



Atty. Dkt. No. 040301-0578

2814- 64

Applicant: Nobutoshi AOKI et al.

Title: SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING
INSULATED GATE FIELD EFFECT TRANSISTOR AND
METHOD OF MANUFACTURING THE SAME

Appl. No.: 09/440,928

Filing Date: 11/16/1999

Examiner: S. Rao

Art Unit: 2814

Allowed: 08/12/2003

23rd/ht re-
1449
11/25/03
10/20

REQUEST FOR INITIALED SB/08

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

In reviewing the above-captioned application file, the undersigned has noted that Applicants' PTO Form SB/08 submitted with their Information Disclosure Statement on June 25, 2003, has not been acknowledged by the Patent and Trademark Office.

The Patent and Trademark Office is, therefore, requested to return the initialed form to the undersigned as soon as possible. A copy of the SB/08 is attached herewith for the Examiner's convenience.

Respectfully submitted,

Date 9/4/03

By [Signature] 34371

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